

REMARKS

Reconsideration and allowance of the above-referenced application are respectfully requested.

Upon entry of this amendment, claims 1, 3-8, and 10-28 will remain in the application.

Drawings

The drawings have been amended to obviate the objections to the drawings.

Specification

The specification has been amended to obviate the objections to the specification.

Claim Rejections - 35 USC § 102

Claims 1-28 were rejected under 35 U.S.C. 102(e) as being allegedly anticipated by Roth et al. (PUB. No. US 2002/0078326 A1, hereinafter "Roth").


The claims have been amended to recite that the future file is updated with an updated data address value without terminating an instruction associated with the updated data address value in the pipeline.

Roth is concerned with speculative registers for hardware loops. The speculative registers that hold address values (34A and 34B) are only updated if there is a termination in the pipeline (see FIG. 4). The speculative count register 34C does not store data address values. Accordingly, Applicants submit that independent claims 1, 8, 15 and 22 and their dependencies are allowable.

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Respectfully submitted,

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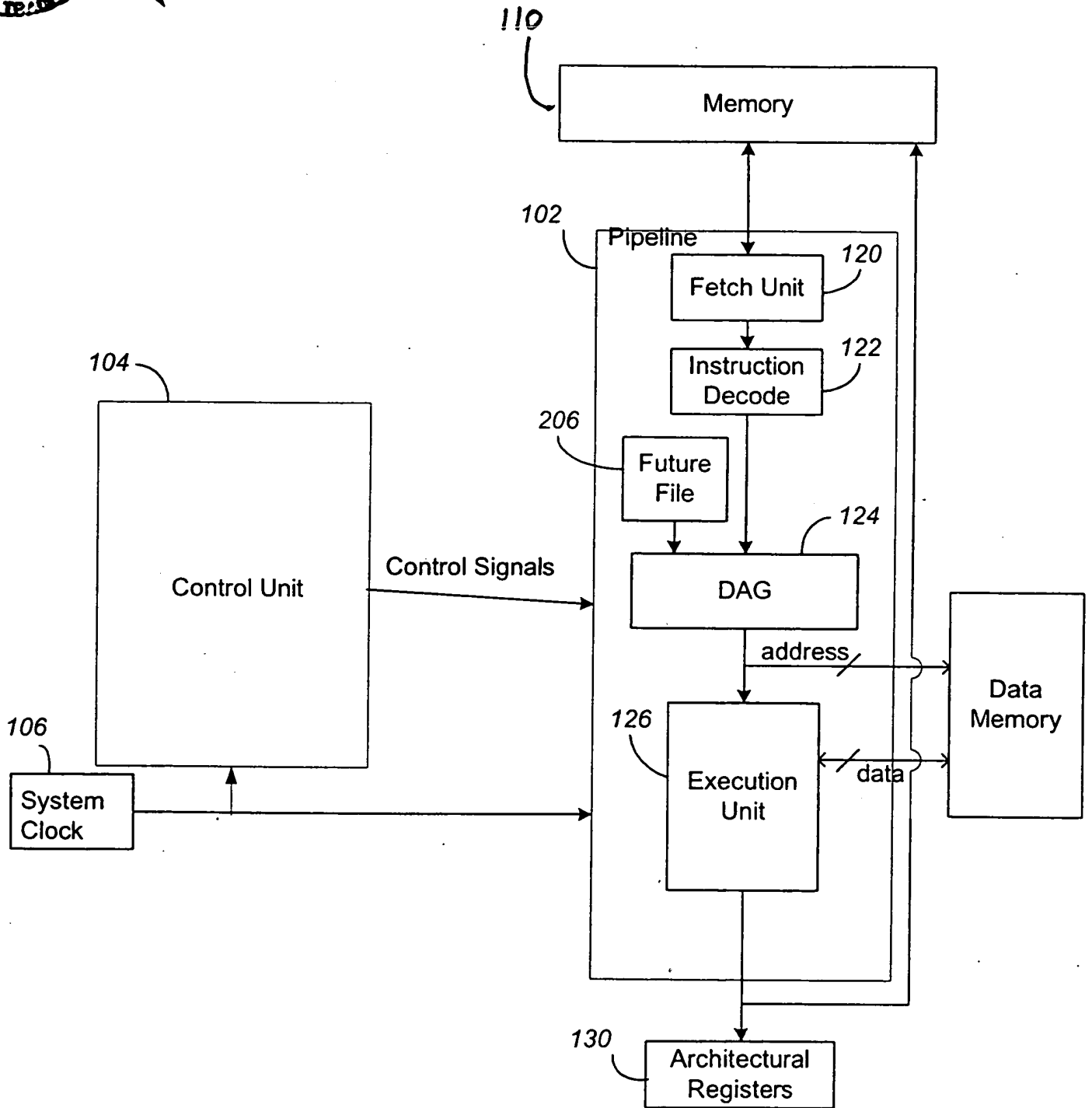


FIG. 1